

Job Opportunity for an FPGA Design Engineer

Silvus Technologies is seeking FPGA Design Engineers to participate in advanced wireless systems R&D.

The Opportunity

Silvus Technologies is a fast-growing, wireless engineering company located in the heart of West Los Angeles. Founded in 2004, Silvus has developed and produced multiple generations of its StreamCaster line of MIMO radios – the most technologically advanced Mobile Ad-Hoc Networking (MANET) solution available. In addition to our successful MANET product line, Silvus also conducts ground breaking research in the area of wireless data communications and has executed on over \$54M in R&D contracts for the US government and commercial entities. Our technical team members possess very strong interdisciplinary capabilities and have provided innovative solutions to a broad range of important challenges for customers in military communications, law enforcement, public safety, commercial broadcasting, unmanned/drone systems and more.

Technical Responsibilities

Our FPGA designers actively participate in all aspects of the development process from concept to field deployment:

- FPGA design architecture definition
- Fixed point system simulations
- RTL coding and test bench development
- FPGA design synthesis and timing closure
- Hardware verification, troubleshooting and field demonstrations

Qualifications

- Must be a U.S. person (permanent resident or citizen) due to clients under government contract
- MSEE required
- 3+ years of industry FPGA design experience required
- Proficient in VHDL (preferred) or Verilog
- Background in Digital Signal Processing (DSP) is a plus
- MIMO, wireless systems exposure is a plus
- Experience with Xilinx FPGAs, SoCs and the Vivado IDE is a plus
- Experience with multiple clock-domain, high-speed, high-utilization FPGA designs
- Experience with Matlab/C/C++ is a plus
- Ability to pass background check

Contact Information

Qualified candidates should send their resume to jobs@silvustechologies.com.